



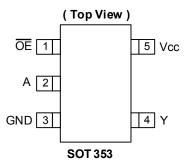
74LVC1G125Q

SINGLE BUFFER GATE WITH 3-STATE OUTPUT

Description

The 74LVC1G125Q an automotive-compliant single, non-inverting buffer/bus driver with a 3-state output. The output enters a high-impedance state when a HIGH level is applied to the output enable $\overline{\rm (OE)}$ pin. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V, allowing this device to be used in a mixed-voltage environment. The device is fully specified for partial power-down applications using $I_{\rm OFF}$. The $I_{\rm OFF}$ circuitry disables the output preventing damaging current backflow when the device is powered down.

Pin Assignments



Features

- Grade 1 Ambient Temperature Operation: -40°C to 125°C
- Wide Supply Voltage Range from 1.65V to 5.5V
- ± 24mA Output Drive at 3.3V
- CMOS Low Power Consumption
- I_{OFF} Supports Partial Power-Down Mode Operation
- Inputs Accept up to 5.5V Regardless of Vcc Level.
- ESD Protection Tested per AEC-Q100
- Exceeds 2000V Human Body Model (AEC Q100-002)
- Exceeds 1000V Charged Device Model (AEC Q100-011)
- Latch-Up Exceeds 100mA (AEC Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74LVC1G125Q0Q is suitable for automotive applications requiring specific change control and is AEC-Q100 qualified, has a grade 1 -40°C to 125°C temperature rating, is PPAP capable, and is manufactured in IATF16949:2016 certified facilities.

Applications

- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide Array of Products such as:
 - Automotive Applications within Grade 1 Temperature Range
 - Industrial Computing/Controls/Automation
 - High Reliability Networking/Communications
 - Industrial/Agricultural Equipment

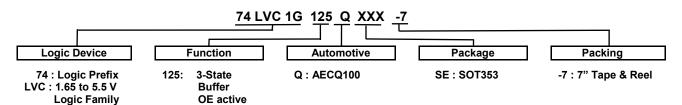
Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

-7



Ordering Information (Note 4)



1G : One Gate LOW Package Package Package 7" Tape and Reel **Part Number** Code (Notes 5 & 6) Size Quantity Part Number Suffix

SOT353 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html. Notes:

5. Pad layout as shown in Diodes Inc. suggested pad layouts, which can be found on our website at see http://www.diodes.com/package-outlines.html.

2.0mm × 2.0mm × 1.1mm

0.65 mm lead pitch

6. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf.

Pin Descriptions

74LVC1G125QSE-7

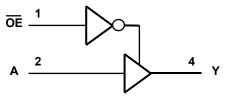
Pin Name	Description	
ŌĒ	Output Enable	
Α	Data Input	
GND	Ground	
Y	Data Output	
V _{CC}	Supply Voltage	

SE

Function Table

Inp	Output	
ŌĒ	Α	Y
L	Н	Н
L	L	L
Н	X	Z

Logic Diagram



3000/Tape & Reel



Absolute Maximum Ratings (Notes 7 & 8) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or I _{OFF} State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State.	-0.5 to V _{CC} +0.5	V
I _{IK}	Input Clamp Current V _I < 0	-50	mA
I _{OK}	Output Clamp Current	-50	mA
Io	Continuous Output Current	±50	mA
I _{CC} , I _{GN}	Continuous Current Through V _{CC} or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°°C
T _{STG}	Storage Temperature	-65 to +150	°C

Notes:

Recommended Operating Conditions (Note 9) (@TA = +25°C, unless otherwise specified.)

Symbol	Parameter		Min	Max	Unit
V	Operating Voltage	Operating	1.65	5.5	>
Vcc	Operating voltage	Data Retention Only	1.5	_	>
		V _{CC} = 1.65V to 1.95V	0.65 × V _{CC}	_	
\/	High-Level Input Voltage	V _{CC} = 2.3V to 2.7V	1.7	_	V
V_{IH}	High-Level input voltage	V _{CC} = 3V to 3.6V	2	_	V
		V _{CC} = 4.5V to 5.5V	0.7 × V _{CC}	_	
		V _{CC} = 1.65V to 1.95V	_	0.35 × V _{CC}	
\ /	Low Lovel Input Voltage	V _{CC} = 2.3V to 2.7V	_	0.7	V
V_{IL}	Low-Level Input Voltage	V _{CC} = 3V to 3.6V	_	0.8	V
		V _{CC} = 4.5V to 5.5V	_	0.3 × V _{CC}	
Vı	Input Voltage		0	5.5	V
Vo	Output Voltage		0	Vcc	V
	V _{CC} = 1.65V	_	-4		
		V _{CC} = 2.3V	_	-8	mA
Lead	High-Level Output Current	V _{CC} = 2.7V	_	-12	
Іон		V2V	_	-16	
		V _{CC} = 3V	_	-24	
		V _{CC} = 4.5V	_	-32	
		V _{CC} = 1.65V	_	4	
		V _{CC} = 2.3V	_	8	
I	Low-Level Output Current	V _{CC} = 2.7V	_	12	mA
I _{OL}	Low-Level Output Current	V - 2V	_	16	IIIA
		V _{CC} = 3V	_	24	
		V _{CC} = 4.5V	_	32	
		V_{CC} = 1.8V ± 0.15V, 2.5V ± 0.2V	_	20	_
$\Delta t/\Delta V$	Input Transition Rise or Fall Rate	$V_{CC} = 3.3 V \pm 0.3 V$	_	10	ns/V
		V _{CC} = 5V ± 0.5V	_	5	
T _A	Operating Free-Air Temperature	_	-40	+125	°C

Note: 9. Unused inputs should be held at V_{CC} or Ground.

^{7.} Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

^{8.} Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.



Electrical Characteristics (All typical values are at V_{CC} = 3.3V, T_A = +25°C)

Cumbal	Parameter	Took Co	onditions	V	-40°	C to +125	°C	Unit
Symbol	Parameter	rest Co	onations	V _{CC}	Min	Тур	Max	Unit
			I _{OH} = -100μA	1.65V to 5.5V	V _{CC} - 0.1	_	_	
			$I_{OH} = -4mA$	1.65V	0.95	_	_	
V	High Lovel Output Voltage	\\\ = \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	$I_{OH} = -8mA$	2.3V	17	_	_	V
V _{OH}	High Level Output Voltage	$V_I = V_{IH}$ or V_{IL}	$I_{OH} = -12mA$	2.7V	1.9	_	_	V
			I _{OH} = -24mA	3V	2.0	_	_	
			$I_{OH} = -32mA$	4.5V	3.4	_	_	
			I _{OL} = 100μA	1.65V to 5.5V	1	_	0.1	
		$V_{I} = V_{IH}$ or V_{IL}	$I_{OL} = 4mA$	1.65V	_	_	0.7	V
.,			I _{OL} = 8mA	2.3V	_	_	0.45	
V _{OL}	Low Level Output Voltage		I _{OL} = 12mA	2.7V	_	_	0.6	
			I _{OL} = 24mA	3V	_	_	8.0	
			I _{OL} = 32mA	4.5V		_	0.8	
I _I	Input Current	V _I = 5.5V or GN	ID	0 to 5.5V	_	± 0.1	±1	μA
I _{OFF}	Power Down Leakage Current	$V_1 \text{ or } V_0 = 5.5V$		0V	_	_	±2	μA
l _{OZ}	Z-State Leakage Current	V _O = Ground to	5.5V	3.6V	_	_	±2	μA
Icc	Supply Current	V _I = 5.5V or GND I _O =0		5.5V	_	0.1	4	μA
Δlcc	Additional Supply Current		One input at V _{CC} –0.6V Other inputs at V _{CC} or GND			_	500	μA
Ci	Input Capacitance	$V_i = V_{CC} - or G$	ND	3.3V	_	3.5	_	pF

Package Characteristics

Symbol	Parameter	Test Conditions	V _{CC}	Min	Тур.	Max	Unit
θја	Thermal Resistance Junction-to-Ambient	SOT353	Note 10	1	371	1	°C/W
θιс	Thermal Resistance Junction-to-Case	SOT353	Note 10	1	143	1	°C/W

Note: 10. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



Switching Characteristics

Figure 1 Typical Values at T_A = +25°C and nominal voltages 1.8V, 2.5V, 2.7V, 3.3V, and 5.0V.

Parameter	From	То	V	٦	T _A = -40°C to +125°C	;	Unit	
Input Input	Output	V _{CC}	Min	Тур	Max	Unit		
			1.8V ± 0.15V	1.0	3.3	10.5		
			2.5V ± 0.2V	0.5	2.2	7.0		
t_{pd}	Α	Y	2.7V	0.5	2.5	7.0	ns	
·			3.3V ± 0.3V	0.5	2.1	6.0		
			5.0V ± 0.5V	0.5	1.7	5.5		
			1.8 V ± 0.15V	1.0	4.1	12.0		
			2.5V ± 0.2V	0.5	2.8	8.5		
t _{en}	ŌE	Y	2.7V	0.5	3.3	8.5	ns	
			3.3V ± 0.3V	0.5	2.4	7.0		
			5.0V ± 0.5V	0.5	2.1	6.5		
			1.8V ± 0.15V	1.0	4.3	12.0		
			2.5V ± 0.2V	0.5	2.7	6.5		
$t_{\sf dis}$	ŌĒ	ŌE	Y	2.7V	0.5	3.0	6.5	ns
			3.3V ± 0.3V	0.5	3.1	6.5		
			5.0V ± 0.5V	0.5	2.2	5.5		

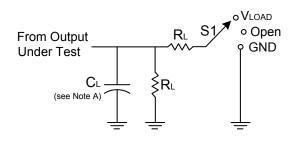
Operating Characteristics

T_A = +25°C

	Parameter		Test Conditions	V _{CC} = 1.8V Typ	V _{CC} = 2.5V Typ	V _{CC} = 3.3V Typ	V _{CC} = 5V Typ	Unit
0	Power Dissipation	Outputs Enabled	f = 10MHz	19	19	19	21	pF
C _{pd}	Capacitance	Outputs Disabled	I – TOWINZ	2	2	3	4	ρг

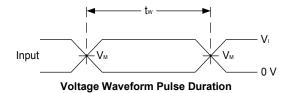


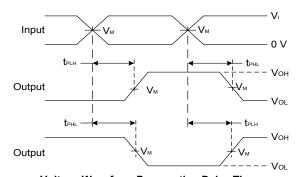
Parameter Measurement Information



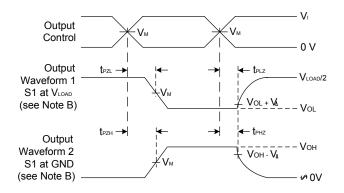
TEST	S1
t _{PLH} /t _{PHL}	Open
t _{PLZ} /t _{PZL}	V_{LOAD}
t _{PHZ} /t _{PZH}	GND

.,	Inputs		.,	V		_	
V _{CC}	Vı	t _r /t _f	VM	V _M V _{LOAD}	CL	R∟	V Δ
1.8V±0.15V	V _{CC}	≤2ns	V _{CC} /2	2 x V _{CC}	30pF	1kΩ	0.15V
2.5V±0.2V	V _{CC}	≤2ns	V _{CC} /2	2 x V _{CC}	30pF	500Ω	0.15V
2.7V	2.7V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
3.3V±0.3V	3V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
5V±0.5V	V _{CC}	≤2.5ns	V _{CC} /2	2 x V _{CC}	50pF	500Ω	0.3V





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs



Voltage Waveform Enable and Disable Times Low and High Level Enabling

Figure 1 Load Circuit and Voltage Waveforms

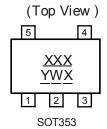
Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 10MHz.
- C. Inputs are measured separately one transition per measurement.
- D. t_{PLZ} and t_{PHZ} are the same as t_{dis}
- E. t_{PZL} and t_{PZH} are the same as t_{EN}
- F. t_{PLH} and t_{PHL} are the same as t_{PD}.



Marking Information

SOT353



XXX: Identification Code

Y : Year 0~9 W : Week: A~Z 1~26 week a~z 27~52 week z represents week 52 and 53

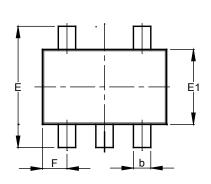
: A~ Z: Internal Code

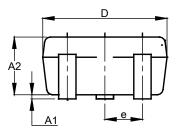
Part Number	Package	Identification Code	
74LVC1G125QSE-7	SOT353	UYQ	

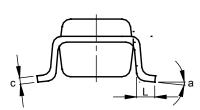


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.







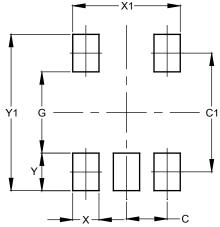
SOT353

SOT353							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.10	0.30	0.25				
С	0.10	0.22	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	C).650 B	SC				
F	0.40	0.45	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All	Dimen	sions	in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.





Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Y	0.600
V4	2 500



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