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April 1st, 20<mark>10</mark> Renesas Electronics Corporation

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3P4MH, 3P6MH

3 A MOLD SCR

The 3P4MH and 3P6MH are P-gate fully diffused mold SCRs with an average on-current of 3 A. The repeat peak off-voltages (and reverse voltages) are 400 V and 600 V.

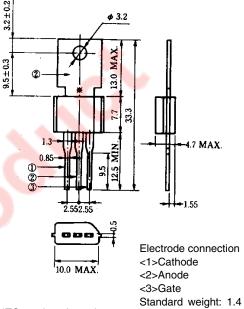
FEATURES

- This transistor features a small and lightweight package and is easy to handle even on the mounting surface due to its TO-202AA dimensions. Processing of lead wires and heatsink (tablet) using jigs is also possible.
- Employs flame-retardant epoxy resin (UL94V-0).

APPLICATIONS

Noncontact switches of consumer electronic euipments, electric equipments, audio quipments, and light indutry equipments

PACKAGE DRAWING (UNIT: mm)



*TC test bench-mark

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Parameter	Symbol	3P4MH	3P6MH	Ratings	Unit
Non-repetitive peak reverse voltage	Vrsm	500	700		$R_{GK} = 1 k\Omega$
Non-repetitive peak off-state voltage	VDSM	500	500 700		$R_{GK} = 1 k\Omega$
Repetitive peak reverse voltage	V_{RRM}	400	600	V	$R_{GK} = 1 k\Omega$
Repetitive peak off-voltage	V_{DRM}	400	600	V	$R_{GK} = 1 k\Omega$
Average on-state current	I _{T(AV)}	3 (Tc = 87°C, Single	Α	Refer to Figure 11.	
Effective on-state current	I _{T(RMS)}	4	Α	-	
Surge on-state current	Ітѕм	65 (f = 50 Hz, Sine	Α	Refer to Figure 2.	
		70 (f = 60 Hz, Sine			
Fusing current	∫it²dt	20 (1 mss	A ² s	_	
Critical rate of rise of on-state current	dl⊤/dt	5	A/μs	_	
Peak gate power dissipation	Рам	2 (f≥50 Hz,	W	Refer to Figure 3.	
Average gate power dissipation	P _{G(AV)}	0	W		
Peak gate forward current	Iгдм	1 (f≥50 Hz,	Α	-	
Peak gate reverse voltage	VRGM	(V	_	
Junction temperature	Tj	–40 to	°C	_	
Storage temperature	Tstg	–55 tp	°C	_	

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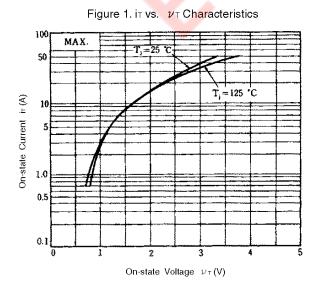
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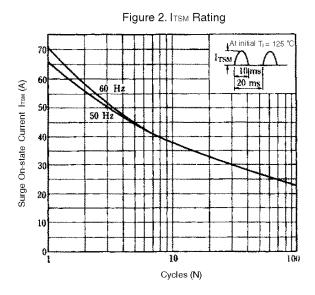


ELECTRICAL CHARACTERISTICS ($T_j = 25$ °C, $R_{GK} = 1 k\Omega$)

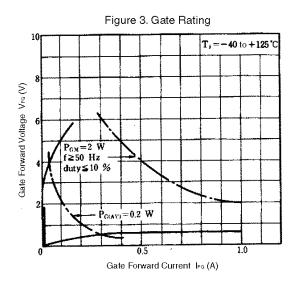
Parameter	Symbol	Conditions		Specifications			Unit	Remarks
				MIN.	TYP.	MAX.		
Repeat peak reverse current	IRRM	V _{RM} = V _{RRM}	T _j = 25°C	-	_	100	μΑ	-
			T _j = 125°C	-	-	2	mA	-
Repeat peak off-current	IDRM	V _{DM} = V _{DRM}	T _j = 25°C	-	-	100	μΑ	-
			T _j = 125°C	-	_	2	mA	_
Critical rate-of-rise of off- state voltage	dV/dt	$T_{j} = 125^{\circ}C, V_{DM} = \frac{2}{3}V_{DRM}$		_	3	_	V/μs	_
On-state voltage	Vтм	Iτ = 10 A		-	-	1.6	V	Refer to Figure 1.
Gate trigger current	Ідт	$V_{DM} = 6 \text{ V}, \text{ RL} = 100 \Omega$		_	_	0.2	mA	Refer to Figure 14.
Gate trigger voltage	V _{GT}	$V_{DM} = 6 \text{ V}, \text{ RL} = 100 \Omega$		_	_	0.8	V	
Gate non-trigger voltage	V _{GD}	$T_{j} = 125^{\circ}C,$ $V_{DM} = \frac{V_{DRM}}{2}$		0.2	-	-3	V	-
Holding current	Ін	V _{DM} = 24 V, I _{TM} = 10 A		-	1	5	mA	-
Commutating turn-off time	Tq	$T_{\rm J} = 125^{\circ} C,$ $I_{\rm T} = 3$ A, dia/dt = 15 A/μs $V_{\rm R} ≥ 25$ V, $V_{\rm DM} = \frac{2}{3} V_{\rm DRM}$ $dV_{\rm D}/dt = 1$ V/μs		-	80) -	μs	-
Thermal resistance	Rth(j-c)	Junction-to-case DC Junction-to-ambient DC			_	8	°C/W	Refer to Figure 13.
	R _{th(j-a)}				_	75		

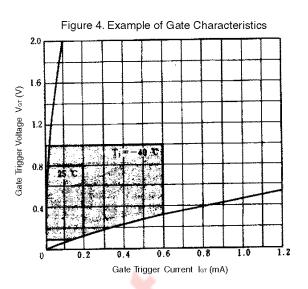
TYPICAL CHARACTERISTICS (Ta = 25°C)

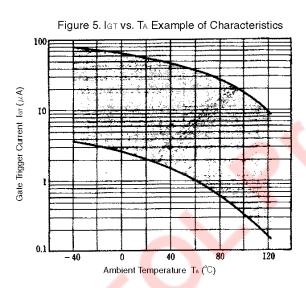


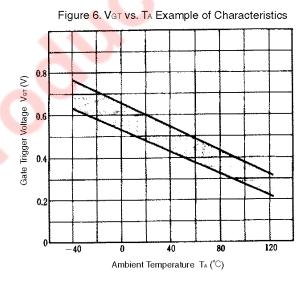


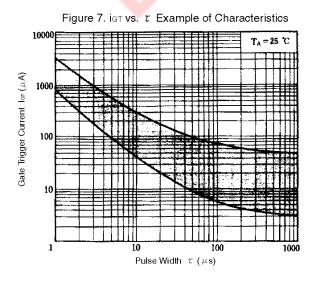


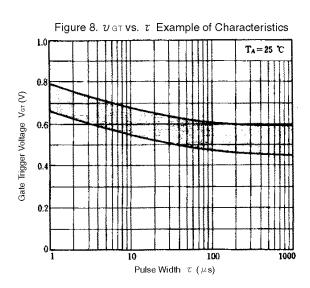


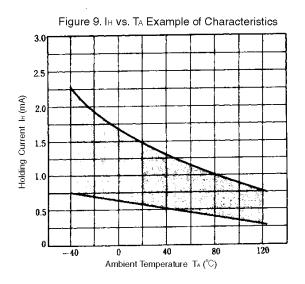


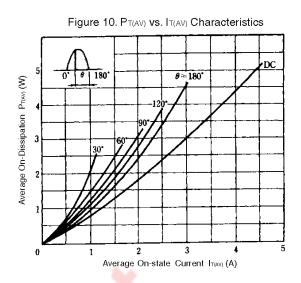


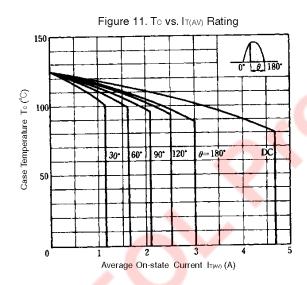












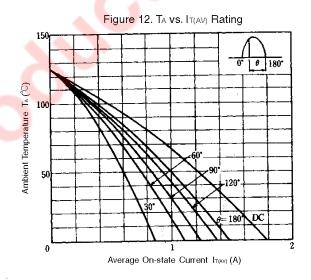
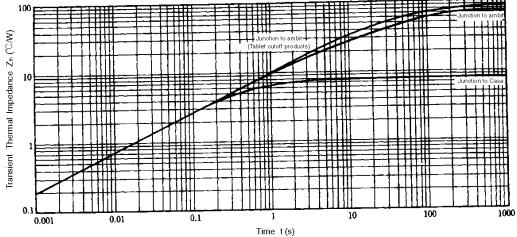


Figure 13. Zth Characteristics



4

[MEMO]



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