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April 1st, 2010 Renesas Electronics Corporation

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RENESAS

HD74HC251

1 of 8 Data Selectors/Multiplexers (with 3-state outputs)

REJ03D0599–0200 (Previous ADE-205-476) Rev.2.00 Jan 31, 2006

Description

This multiplexer features both true (Y) and complement (W) outputs as well as a strobe input. The strobe must be at a low logic level to enable this device. When the strobe input is high, both outputs are in the high impedance state. When enabled, address information on the data select inputs determine which data input is routed to the Y and W outputs.

Features

- High Speed Operation: t_{pd} (A, B, C to Y) = 20 ns typ (C_L = 50 pF)
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$
- Low Input Current: 1 µA max
- Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)
- Ordering Information

Part Name	Package Type	Package Code (Previous Cod <mark>e)</mark>	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC251P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Ρ	—
HD74HC251FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)
HD74HC251RPEL	SOP-16 pin (JEDEC)	PRS <mark>P00</mark> 16DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

Function Table

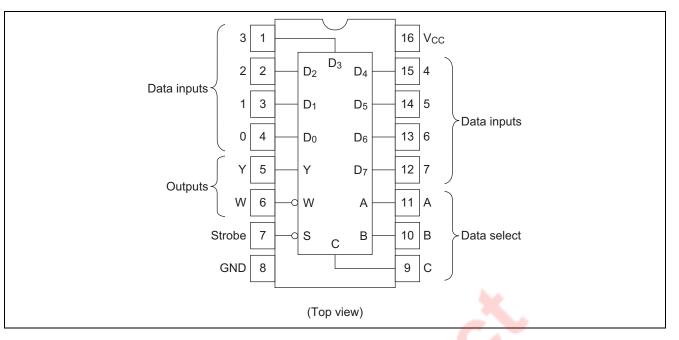
	Inp	Outputs					
	Select		Strobe	Outputs			
С	В	Α	S	Y	W		
Х	х	Х	Н	Z	Z		
L	L	L	L	D ₀	\overline{D}_0		
L	L	Н	L	D ₁	\overline{D}_1		
L	Н	L	L	D ₂	\overline{D}_2		
L	Н	Н	L	D ₃	\overline{D}_3		
Н	L	L	L	D ₄	\overline{D}_4		
Н	L	Н	L	D ₅	\overline{D}_5		
Н	Н	L	L	D ₆	\overline{D}_6		
Н	Н	Н	L	D ₇	\overline{D}_7		

Notes: 1. H: high level, L: low level, X: irrelevant

- 2. Z; high impedance (off-state)
- 3. D_0 through D_7 ; the level of the respective D input.

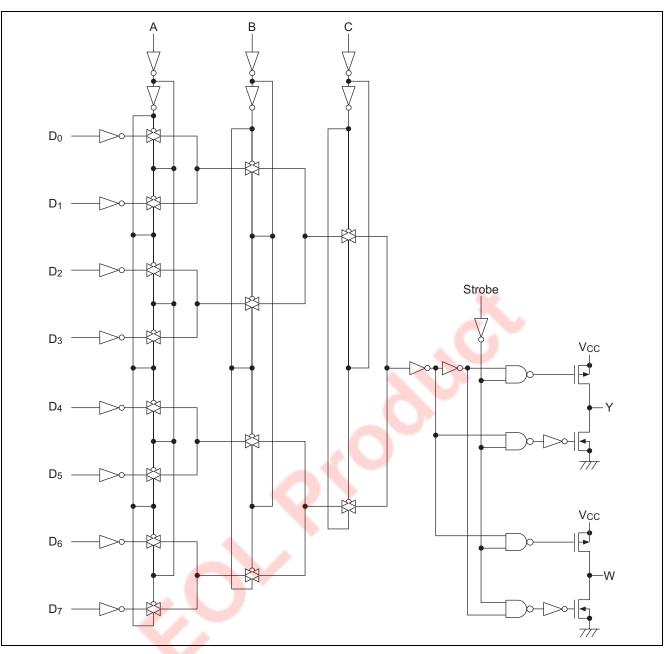


Pin Arrangement



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Logic Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	V _{cc}	-0.5 to 7.0	V
Input / Output voltage	V _{IN} , V _{OUT}	–0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{ік} , І _{ок}	±20	mA
Output current	lo	±25	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±50	mA
Power dissipation	PT	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.



Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	2 to 6	V	
Input / Output voltage	V _{IN} , V _{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
Input rise / fall time ^{*1}	t _r , t _f	0 to 1000	ns	$V_{CC} = 2.0 V$
		0 to 500		$V_{CC} = 4.5 V$
		0 to 400		$V_{CC} = 6.0 V$

Notes: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.

Electrical Characteristics

ltem	Symbol	V _{cc} (V)	Ta = 25°C		Ta = -40 to+85°C		1.1	Test Conditions		
			Min	Тур	Max	Min	Max	Unit	Test Cor	aitions
Input voltage	VIH	2.0	1.5	—	—	1.5	_	V		
		4.5	3.15	—		3.15	—			
		6.0	4.2	—	—	4.2	—			
	VIL	2.0	_	—	0.5		0.5	V		
		4.5	_	—	1.35		1.35			
		6.0	_	—	1.8		1.8 🧹			
Output voltage	V _{OH}	2.0	1.9	2.0	—	1.9	-	V	$Vin = V_{IH} \text{ or } V_{IL}$	I _{OH} = -20 μA
		4.5	4.4	4.5	—	4.4	_			
		6.0	5.9	6.0	—	5.9				
		4.5	4.18	—	—	4.13				I _{ОН} = -4 mA
		6.0	5.68	—	_	5 .63	_			I _{OH} = -5.2 mA
	V _{OL}	2.0	_	0.0	0.1	Ì	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 20 \ \mu A$
		4.5	_	0.0	0.1	-	0.1			
		6.0	_	0.0	0.1		0.1			
		4.5	_	1	0.26		0.33			$I_{OL} = 4 \text{ mA}$
		6.0		_	0.26		0.33			$I_{OL} = 5.2 \text{ mA}$
Off-state output	l _{oz}	6.0	ł		±0.5		±5.0	μΑ	$Vin = V_{IH} \text{ or } V_{IL},$	
current									Vout = V_{CC} or G	ND
Input current	lin	<mark>6.</mark> 0	-	_	±0.1	—	±1.0	μA	Vin = V _{CC} or GND	
Quiescent supply current	I _{CC}	6.0		_	4.0	_	40	μA	$Vin = V_{CC} \text{ or } GN$	D, lout = $0 \mu A$

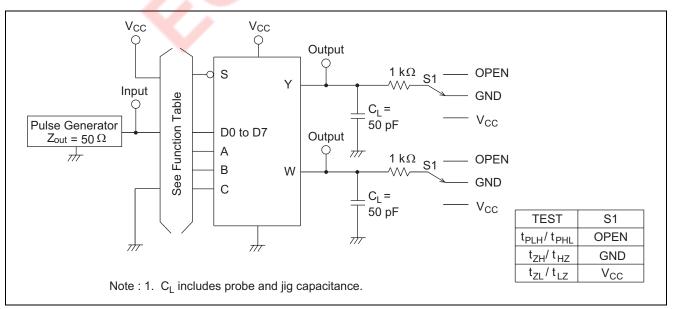


Switching Characteristics

 $(C_L = 50 \text{ pF}, \text{ Input } t_r = t_f = 6 \text{ ns})$

Item	Cumhal	Symbol	V 00	Т	a = 25°	С	Ta = -40	to +85°C	11	To al Oan ditions	
	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions		
Propagation delay	t _{PLH}	2.0	_		205		255	ns	A, B or C to Y		
time	t _{PHL}	4.5		20	41		51				
		6.0	_	_	35		43				
	t _{PLH}	2.0	_	_	205		255	ns	A, B or C to W	1	
	t _{PHL}	4.5	_	20	41	_	51				
		6.0	_	_	35		43				
	t _{PLH}	2.0	_	_	195	_	245	ns	Data to Y		
	t _{PHL}	4.5	_	17	39	_	49				
		6.0	_	_	33	_	42				
	t _{PLH}	2.0	_	_	185	—	230	ns	Data to W		
	t _{PHL}	4.5	_	17	37	—	46				
		6.0	_	_	31	—	39				
Output enable time	t _{ZH}	2.0	_	_	150	—	190	ns	strobe to W	$R_L = 1 \ k\Omega$	
	t _{ZL}	4.5	_	11	30	—	38				
		6.0	_	_	26	_	33				
	t _{ZH}	2.0	_	_	145	_	180 🧹	ns	strobe to Y	$R_L = 1 \ k\Omega$	
	t _{ZL}	4.5	_	11	29	-	36				
		6.0	_	_	25	-	31	5			
Output disable	t _{HZ}	2.0	_	_	220	-	275	ns	strobe to W	$R_L = 1 \ k\Omega$	
time	t _{LZ}	4.5	_	12	44		55				
		6.0			37		47				
	t _{HZ}	2.0	_	_	195	Š	245	ns	strobe to Y	$R_L = 1 \ k\Omega$	
	t _{LZ}	4.5	_	12	39	1	49				
		6.0			33	_	42				
Output rise/fall	t _{TLH}	2.0		Ĩ	75	—	90	ns			
time	t_{THL}	4.5	l	5	15	—	19				
		6.0	Į		13	—	16				
Input capacitance	Cin		-	5	10		10	pF			

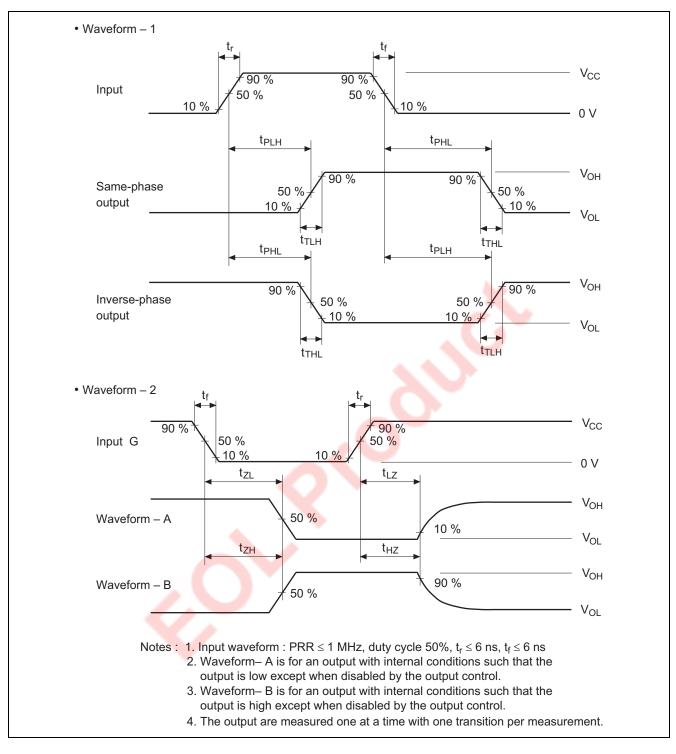
Test Circuit



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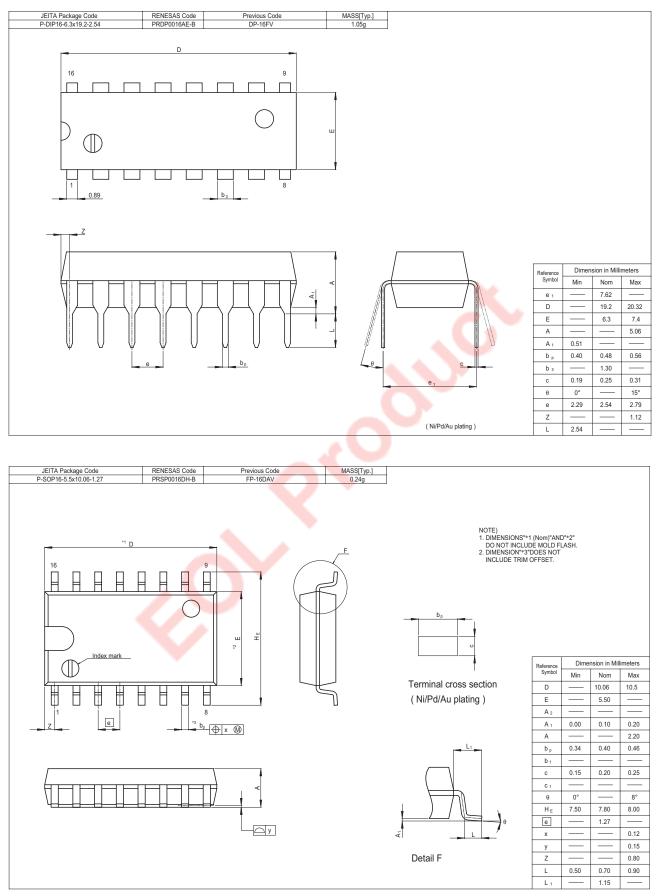


Waveforms



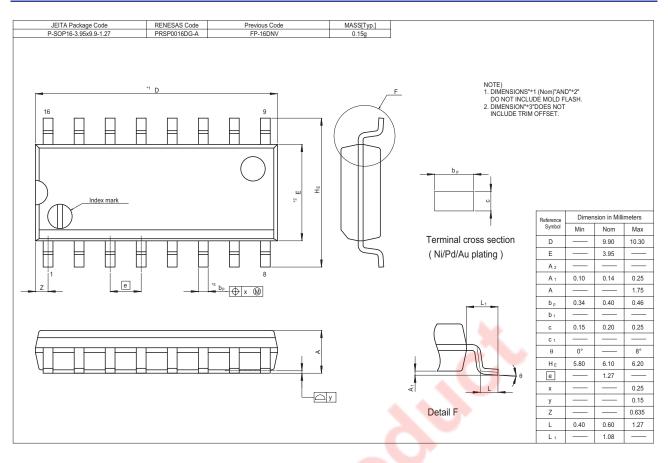


Package Dimensions





HD74HC251



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