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April 1st, 2010 Renesas Electronics Corporation

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HD74HC4543

BCD-to-Seven Segment Latch/Decoder/Driver

REJ03D0655-0200 (Previous ADE-205-544) Rev.2.00 Mar 30, 2006

Description

This circuit contains a 4-bit latch, BCD-to-7 segment decoder, and 7 output drivers. Data on the input pins flow through to the output when the Latch Disable (LE) is high and is latched on the high to low transition of the LE input. The Phase input (Ph) controls the polarity of the 7 segment outputs. When Ph is low the outputs are true 7 segment, and when Ph is high the outputs are inverted 7 segment. When the Phase input is driven by a liquid crystal display (LCD) backplane waveform the segment pins output the correct segment waveform for proper LCD AC drive voltages.

In addition a Blanking input (BI) is provided, which will blank the display.

Features

• High Speed Operation: t_{pd} (A, B, C, D to a - g) = 33 ns typ ($C_L = 50$ pF)

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$

• Low Input Current: 1 μA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC4543P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_
HD74HC4543FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B	FP	EL (2,000 pcs/reel)
HD74HC4543RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.



Function Table

Inputs						Outputs								
LD	BI	Ph*1	D	С	В	Α	а	b	С	d	е	f	g	Display
Х	Н	L	Х	Х	Х	Х	L	L	L	L	L	L	L	Blank
Н	L	L	L	L	L	L	Ι	Н	Н	Н	Н	Н	L	0
Н	L	L	L	L	L	Н	L	Н	Н	L	L	L	L	1
Н	L	L	L	L	Н	L	Н	Н	L	Н	Н	L	Н	2
Н	L	L	L	L	Н	Н	Н	Н	Н	Н	L	L	Н	3
Н	L	L	L	Н	L	L	L	Н	Н	L	L	Н	Н	4
Н	L	L	L	Н	L	Н	Н	L	Н	Н	L	Н	Н	5
Н	L	L	L	Н	Н	L	Н	L	Н	Н	Н	Н	Н	6
Н	L	L	L	Н	Н	Н	Н	Н	Н	L	L	L	L	7
Н	L	L	Н	L	L	L	Н	Н	Н	Н	Н	Н	Н	8
Н	L	L	Н	L	L	Н	Н	Н	Н	Н	L	Н	Н	9
Н	L	L	Н	L	Н	L	L	L	L	L	L	L	L	Blank
Н	L	L	Н	L	Н	Н	L	L	L	L	L	L	L	Blank
Н	L	L	Н	Н	L	L	L	L	L	L	L	L	L	Blank
Н	L	L	Н	Н	L	Н	L	L	L	L	L	L	L	Blank
Н	L	L	Н	Н	Н	L	L	L	L	L	øĽ\	ML	L	Blank
Н	L	L	Н	Н	Н	Н	L	L	L	L	L	L	L	Blank
L	L	L	Х	Х	Х	Х				*2				*1

Notes: 1. For liquid crystal readouts, apply a square wave to Ph.

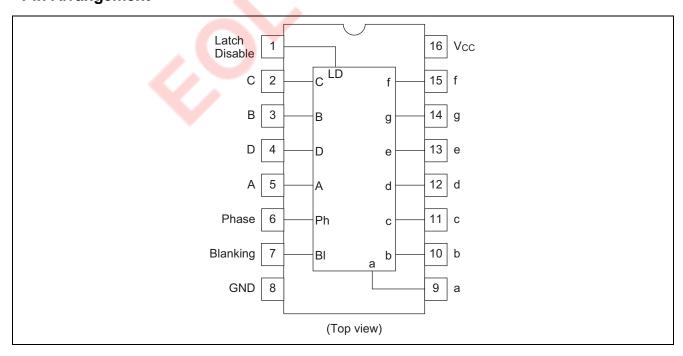
For common cathode LED readouts, select Ph = L. For common anode LED readouts, select Ph = H

2. Depends upon the BCD coder previously applied when LD = H

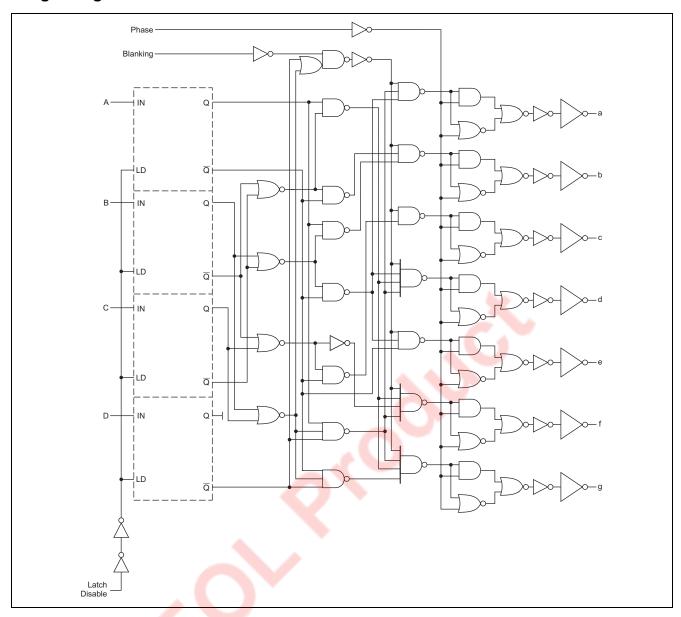




Pin Arrangement



Logic Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	Vcc	-0.5 to 7.0	V
Input / Output voltage	V _{IN} , V _{OUT}	-0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{IK} , I _{OK}	±20	mA
Output current	I _{OUT}	±25	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±50	mA
Power dissipation	P _T	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V_{CC}	2 to 6	V	
Input / Output voltage	V_{IN}, V_{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
		0 to 1000		$V_{CC} = 2.0 \text{ V}$
Input rise / fall time*1	t _r , t _f	0 to 500	ns	$V_{CC} = 4.5 \text{ V}$
		0 to 400		V _{CC} = 6.0 V

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

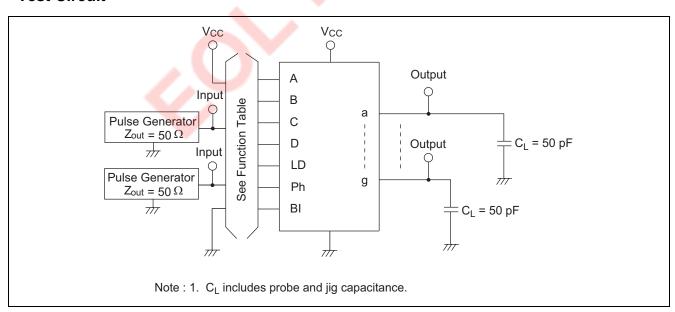
Electrical Characteristics

			Т	a = 25°	С	Ta = -40 to+85°C				
Item	Symbol	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit	Test Con	ditions
Input voltage	V _{IH}	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	-	_	3.15	_			
		6.0	4.2	1	_	4.2	_		3	
	V_{IL}	2.0	-	-	0.5	—	0.5	V		
		4.5	1	1	1.35	_	1.35			
		6.0	1	1	1.8	_	1.8			
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9	1	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4		-5		
		6.0	5.9	6.0	_	5.9	-			
		4.5	4.18	_	_	4.13	3			$I_{OH} = -4 \text{ mA}$
		6.0	5.68	_	_	5.63	_			$I_{OH} = -5.2 \text{ mA}$
	V _{OL}	2.0	_	0.0	0.1		0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 20 \mu A$
		4.5	_	0.0	0.1	_	0.1			
		6.0	_	0.0	0.1	_	0.1			
		4.5	_	-	0.26	_	0.33			$I_{OH} = 4 \text{ mA}$
		6.0	_	_	0.26	_	0.33			$I_{OH} = 5.2 \text{ mA}$
Input current	lin	6.0			±0.1	_	±1.0	μΑ	Vin = V _{CC} or GND	
Quiescent supply current	I _{CC}	6.0		-	4.0	_	40	μА	Vin = V _{CC} or GN	D, $lout = 0 \mu A$

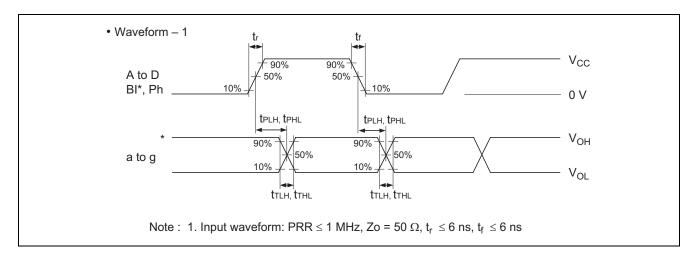
Switching Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

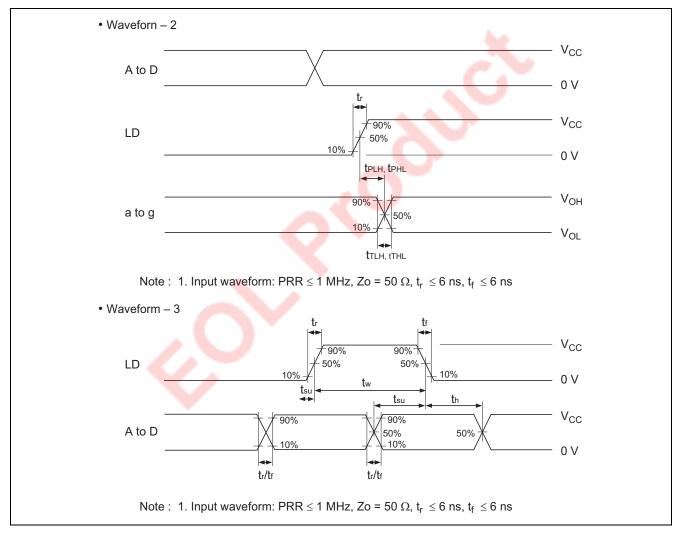
			Т	a = 25°	С	Ta = -40 to +85°C			
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0	_	_	400	_	500	ns	A, B, C or D to a – g
time	t _{PHL}	4.5	_	33	80	_	100		
		6.0	_	_	68	_	86		
	t _{PLH}	2.0	_	_	300	_	380	ns	Blanking to a – g
	t _{PHL}	4.5	_	22	60	_	76		
		6.0	_	_	52	_	66		
	t _{PLH}	2.0	_	_	300	_	380	ns	Phase to a – g
	t _{PHL}	4.5	_	18	60	_	76		
		6.0	_	-	52	_	66		
	t _{PLH}	2.0	_	_	400	_	500	ns	Latch Disable to a – g
	t _{PHL}	4.5	_	35	80	_	100		
		6.0	_	_	68	_	86		
Pulse width	t _w	2.0	80	_	_	100	_	ns	
		4.5	16	5	_	20	_		
		6.0	14	_	_	17	_	- 1	
Setup time	t _{su}	2.0	100	_	_	125	_	ns	
		4.5	20	2	_	25	_		
		6.0	17	_	_	21	- 1		
Hold time	t _h	2.0	50	_	_	65	<u> </u> ←	ns	
		4.5	10	1	_	13	7-7		
		6.0	9	_	_	11	-		
Output rise/fall	t _{TLH}	2.0	_	-	75		95	ns	
time	t_{THL}	4.5	_	5	15		19		
		6.0	_	_	13		16		
Input capacitance	Cin	_	_	5	10		10	pF	

Test Circuit

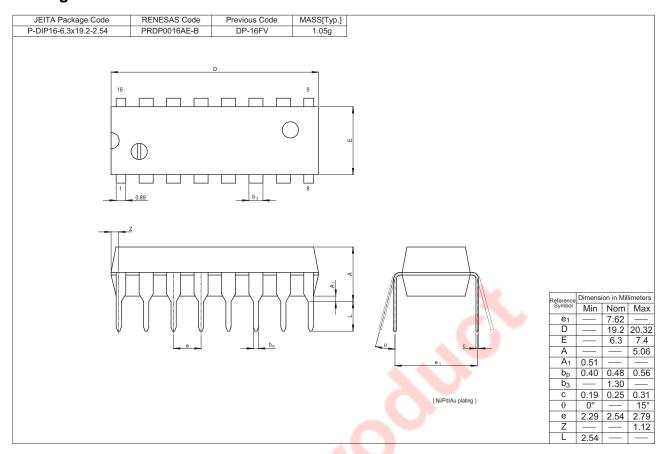


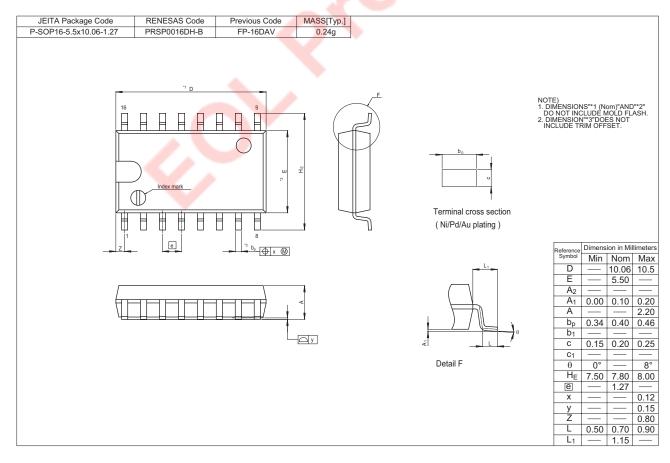
Waveforms



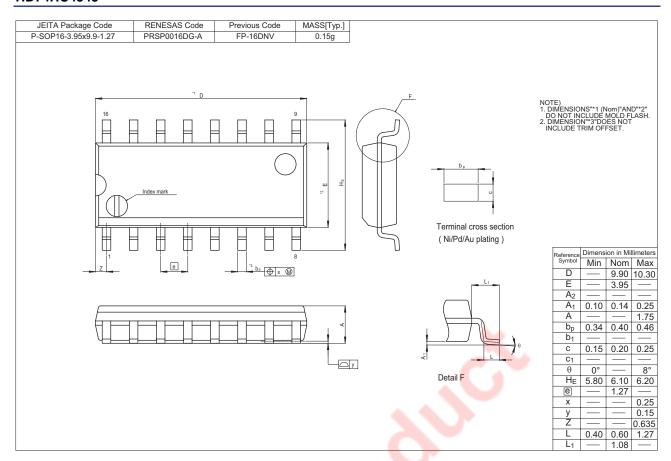


Package Dimensions





HD74HC4543



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