

HD74LV1G125A

Bus Buffer Gate with 3-state Output

R04DS0025EJ0800 Rev.8.00 Jan 10, 2014

Description

The HD74LV1G125A has a bus buffer gate with 3–state output in a 5 pin package. Output is disabled when the associated output enable (\overline{OE}) input is high. To ensure the high impedance state during power up or power down, \overline{OE} should be connected to V_{CC} through a pull-down resistor; the minimum value of the resistor is determined by the current sourcing capability of the driver. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV125A

Supply voltage range: 1.65 to 5.5 V

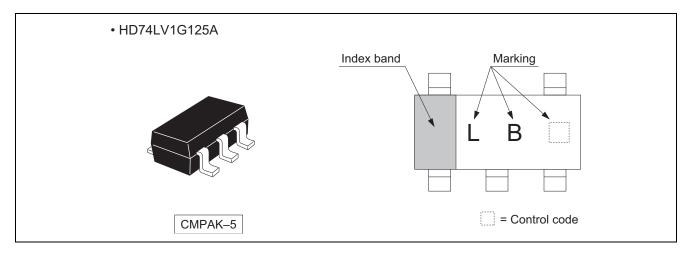
Operating temperature range : -40 to +85°C

- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V) All outputs V_{O} (Max.) = 5.5 V (@ V_{CC} = 0 V, Output : Z)
- Output current ± 6 mA (@V_{CC} = 3.0 V to 3.6 V), ± 12 mA (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

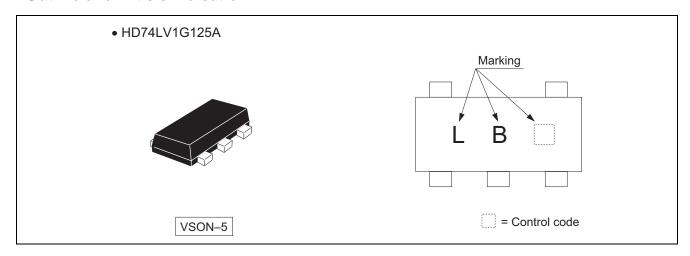
Part Name	Package Type	Package Code	Package	Taping Abbreviation	
1 art Hame	r ackage rype	(Previous Code)	Abbreviation	(Quantity)	
HD74LV1G125ACME	CMPAK-5 pin	PTSP0005ZC-A	CM	E (3000 pcs/reel)	
HD74LV IG125ACIVIE	CIVIPAN-5 PIII	(CMPAK-5V)	Civi		
HD74LV1G125AVSE	VSON-5 pin	PUSN0005KA-A	VS	E (3000 pcs/reel)	
TID14LV IG125AVSE	V 3ON-5 PIII	(TNP-5DV)	VS		

Note: Please consult the sales office for the above package availability.

Outline and Article Indication



Outline and Article Indication

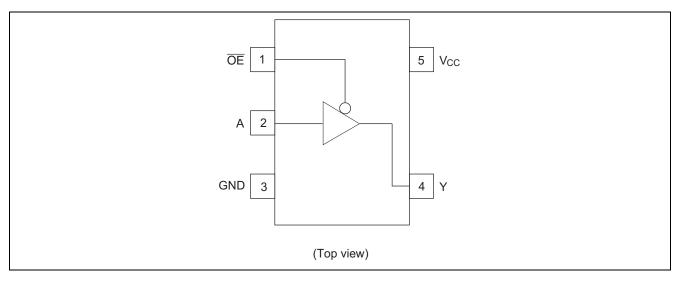


Function Table

Inp	outs	Output Y	
ŌĒ	Α		
L	Н	Н	
L	L	L	
Н	X	Z	

H : High level
L : Low level
X : Immaterial
Z : High impedance

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	Vı	-0.5 to 7.0	V	
Output voltage range *1, 2	Vo	-0.5 to V _{CC} + 0.5	V	Output : H or L
Output voltage range	VO	-0.5 to 7.0	V	V _{CC} : OFF or Output : Z
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	I _{OK}	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	Io	±25	mA	$V_O = 0$ to V_{CC}
Continuous current through V _{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) *3	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	M	0	V _{CC}	V	
Output voltage range	Vo	0	5.5	V	Output : Z
		1	1		$V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$
		_	2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
	I _{OL}	_	6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
Output current		_	12	mA	$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Output current		_	-1		V _{CC} = 1.65 to 1.95 V
		1	-2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
	Іон	1	-6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		1	-12		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
		0	300		$V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$
Input transition rise or fall rate	Δt / Δν	0	200	no / \/	$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
Input transition rise or fall rate	Δτ / Δν	0	100	ns / V	$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		0	20		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Jan 10, 2014

Electrical Characteristics

• $Ta = -40 \text{ to } 85^{\circ}\text{C}$

Item	Symbol	V _{CC} (V) *	Min	Тур	Max	Unit	Test condition
		1.65 to 1.95	V _{CC} ×0.75	_	_		
	\/	2.3 to 2.7	V _{CC} ×0.7	_	_		
	V _{IH}	3.0 to 3.6	V _{CC} ×0.7	_	_		
lanut voltaga		4.5 to 5.5	V _{CC} ×0.7	_	_	V	
Input voltage		1.65 to 1.95	_	_	V _{CC} ×0.25	V	
	\/	2.3 to 2.7	_	_	V _{CC} ×0.3		
	V _{IL}	3.0 to 3.6	_	_	V _{CC} ×0.3		
		4.5 to 5.5	_	_	V _{CC} ×0.3		
		1.8	_	0.25	_		
Hyotoroois voltage	\/	2.5	_	0.30	_	V	$V_T^+ - V_T^-$
Hysteresis voltage	V _H	3.3	_	0.35	_	V	$V_T - V_T$
		5.0	_	0.45	_		
		Min to Max	V _{CC} -0.1	_	_		I _{OH} = -50 μA
	V _{ОН}	1.65	1.4	_	_		I _{OH} = -1 mA
		2.3	2.0	_	_		I _{OH} = -2 mA
		3.0	2.48	_	_		I _{OH} = -6 mA
Output voltage		4.5	3.8	_	_	V	I _{OH} = -12 mA
Output voltage		Min to Max	_	_	0.1	V	$I_{OL} = 50 \mu\text{A}$
		1.65	_		0.3		I _{OL} = 1 mA
	V_{OL}	2.3	_	_	0.4		I _{OL} = 2 mA
		3.0	_	_	0.44		I _{OL} = 6 mA
		4.5	_		0.55		I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Off state output current	I _{OZ}	Min to Max	_		±5	μΑ	V _O = 5.5 V or GND
Quiescent supply current	Icc	5.5	_	_	10	μΑ	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
Output leakage current	I _{OFF}	0	_	_	5	μΑ	V_{IN} or $V_O = 0$ to 5.5 V
Input capacitance	C _{IN}	3.3	_	3.0	_	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

$\bullet \quad V_{CC} = 1.8 \pm 0.15 \ V$

Item	Symbol	Ta = 25°C			Ta = -40	Ta = -40 to 85°C		Test	FROM	то
item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	13.5	23.5	1.0	26.0	no	C _L = 15 pF	Α	V
delay time	t _{PHL}	_	19.0	33.0	1.0	36.0	ns	C _L = 50 pF	A	ī
Enable time	t _{zH}	_	13.7	26.5	1.0	29.0		C _L = 15 pF	ŌĒ	V
Enable time	t _{ZL}	_	20.5	36.0	1.0	38.0	ns	C _L = 50 pF	OE	Y
Disable time	t _{HZ}	_	8.3	20.0	1.0	22.5		C _L = 15 pF	ŌĒ	Y
Disable time	t_{LZ}	_	13.0	29.5	1.0	32.0	ns	C _L = 50 pF	OE	

$\bullet \quad V_{CC} = 2.5 \pm 0.2 \ V$

ltom	Cumbal	Ta = 25°C			Ta = -40 to 85°C		Unit	Test	FROM	то
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	6.8	13.0	1.0	15.5	no	C _L = 15 pF	Α	>
delay time	t _{PHL}	_	8.7	16.5	1.0	18.5	ns	C _L = 50 pF	A	ī
Enable time	t _{ZH}	_	7.0	13.0	1.0	15.5	2	C _L = 15 pF	ŌĒ	Y
Enable time	t _{ZL}	_	8.8	16.5	1.0	18.5	ns	C _L = 50 pF		
Disable time	t _{HZ}	_	5.1	14.7	1.0	17.0	20	C _L = 15 pF	ŌĒ	Y
Disable time	t _{LZ}	_	7.3	18.2	1.0	20.5	ns	C _L = 50 pF) L	

• $V_{CC} = 3.3 \pm 0.3 \text{ V}$

Itama	Cumbal	Ta = 25°C			Ta = -40 to 85°C		l lmi4	Test	FROM	то
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	4.8	8.0	1.0	9.5	ns	C _L = 15 pF	۸	
delay time	t _{PHL}	_	6.1	11.5	1.0	13.0	ns	C _L = 50 pF	Α	ī
Enable time	t _{zH}	_	4.8	8.0	1.0	9.5		C _L = 15 pF	ŌĒ	Y
Enable time	t _{ZL}	_	6.2	11.5	1.0	13.0	ns	C _L = 50 pF		
Disable time	t _{HZ}	_	4.1	9.7	1.0	11.5		C _L = 15 pF	ŌĒ	V
Disable time	t _{LZ}	_	5.5	13.2	1.0	15.0	ns	C _L = 50 pF) 	Ť

• $V_{CC} = 5.0 \pm 0.5 \text{ V}$

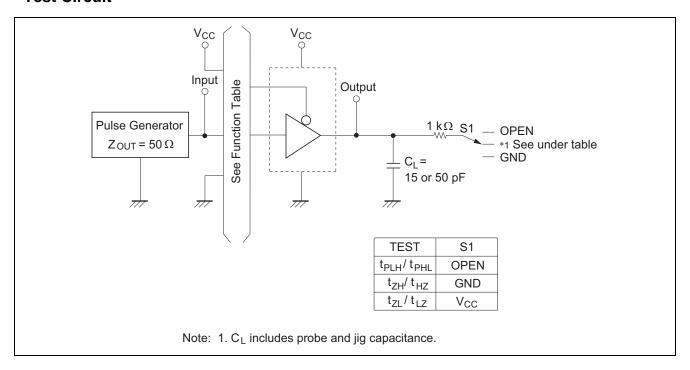
Itama	Cumbal		Ta = 25°C			Ta = -40 to 85°C		Test	FROM	то
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	3.4	5.5	1.0	6.5		C _L = 15 pF	Α	Y
delay time	t _{PHL}	_	4.3	7.5	1.0	8.5	ns	C _L = 50 pF	A	
Enable time	t _{zH}	_	3.4	5.1	1.0	6.0		C _L = 15 pF	ŌĒ	Y
Enable time	t _{ZL}	_	4.4	7.1	1.0	8.0	ns	C _L = 50 pF	OE	
Disable time	t _{HZ}	_	3.2	6.8	1.0 8.0		C _L = 15 pF		V	
Disable time	t _{LZ}	_	4.0	8.8	1.0	10.0	ns	C _L = 50 pF	ŌĒ	Ť

Operating Characteristics

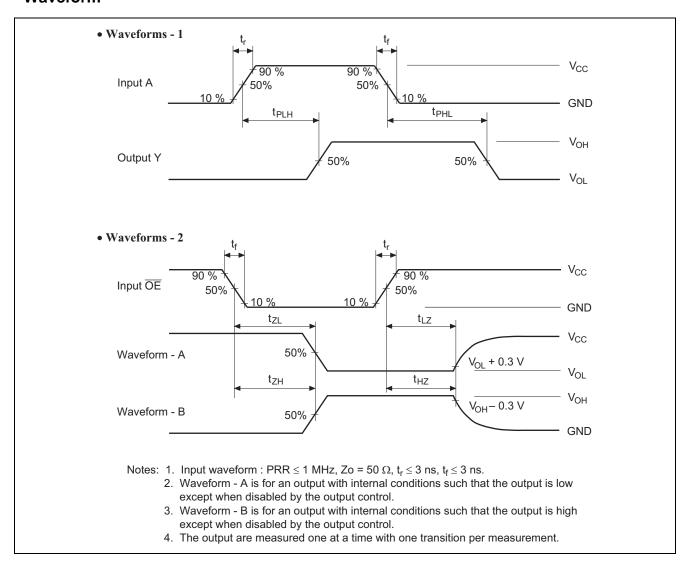
$\bullet \quad C_L = 50 \text{ pF}$

ltom	Cumbal	V 00	Ta = 25°C			l lmi4	Test Conditions	
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	rest Conditions	
Power dissipation		3.3	_	10.5	_	pF	6 40 MH-	
capacitance	CPD	5.0	_	11.5	_	ρг	f = 10 MHz	

Test Circuit

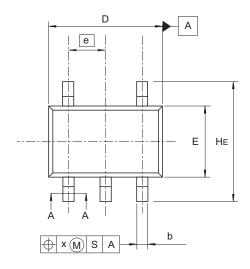


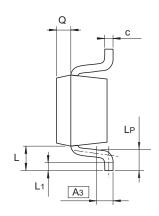
Waveform

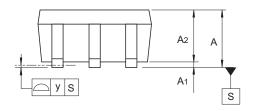


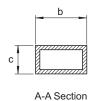
Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-88A	PTSP0005ZC-A	CMPAK-5 / CMPAK-5V	0.006



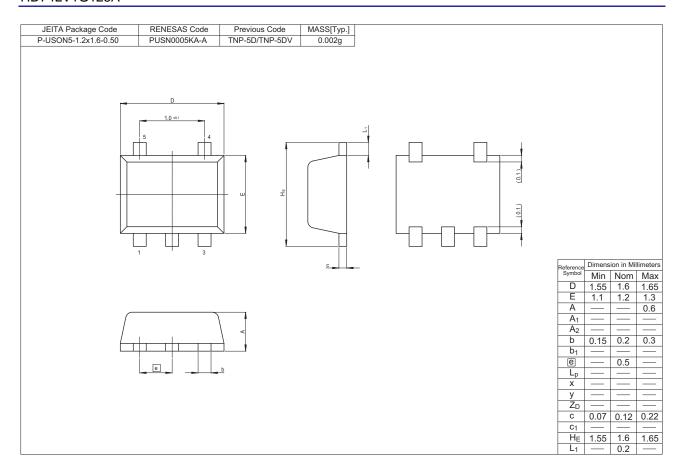






Reference	Dimensi	ons in mi	llimeters	
Symbol	Min	Nom	Max	
Α	0.8		1.1	
A ₁	0		0.1	
A ₂	0.8	0.9	1.0	
A_3		0.25		
b	0.15	0.22	0.3	
С	0.1	0.13	0.15	
D	1.8	2.0	2.2	
E	1.15	1.25	1.35	
е	_	0.65		
HE	1.8	2.1	2.4	
L	0.3	_	0.7	
L ₁	0.1	_	0.5	
LP	0.2		0.6	
Х	_		0.05	
У			0.05	
Q		0.25	_	

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