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RENESAS HD74LVC04

Hex Inverters

REJ03D0343-0300Z (Previous ADE-205-062B (Z)) Rev.3.00 Jul. 22, 2004

Description

The HD74LVC04 has six inverters in a 14 pin package. Low voltage and high speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

Features

- $V_{CC} = 2.0 \text{ V to } 5.5 \text{ V}$
- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V)
- Typical V_{OL} ground bounce < 0.8 V (@V_{CC} = 3.3 V, Ta = 25°C)
- Typical V_{OH} undershoot > 2.0 V (@V_{CC} = 3.3 V, Ta = 25°C)
- High output current $\pm 24 \text{ mA}$ (@V_{CC} = 3.0 V to 5.5 V)
- Ordering Information

Part Name	t Name Package Type		Package Abbreviation	Taping Abbreviation (Quantity)		
HD74LVC04FPEL	SOP–14 pin (JEITA)	FP-14DAV	FP	EL (2,000 pcs/reel)		
HD74LVC04TELL	TSSOP-14 pin	TTP-14DV	Т	ELL (2,000 pcs/reel)		

Note: Please consult the sales office for the above package availability.

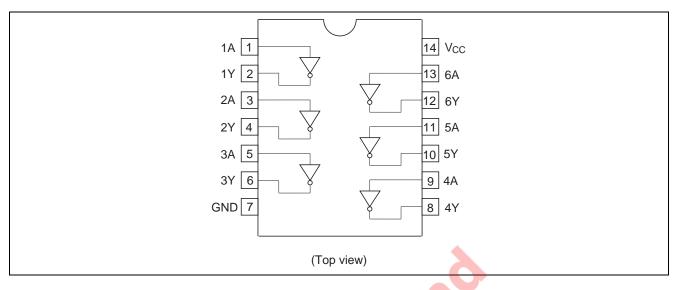
Function Table

Inpu	t A	Output Y	
Н		L	
L		Н	
H:	High level		
L:	Low level		



HD74LVC04

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	Vcc	–0.5 to 6.0	V	
Input diode current	I _{IK}	-50	mA	$V_1 = -0.5 V$
Input voltage	VI	–0.5 to 6.0	V	
Output diode current	Ι _{ΟΚ}	-50	mA	$V_{\rm O} = -0.5 \ V$
		50		$V_{\rm O} = V_{\rm CC} + 0.5 \ V$
Output voltage	Vo	–0.5 to V _{CC} +0.5	V	
Output current	lo	±50	mA	
V _{CC} , GND current / pin	I _{CC} or I _{GND}	100	mA	
Storage temperature	Tstg	-65 to +150	°C	

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	Vcc	1.5 to 5.5	V	Data retention
		2.0 to 5.5		At operation
Input / Output voltage	VI	0 to 5.5	V	A
	Vo	0 to V _{CC}		Y
Operating temperature	Та	-40 to 85	°C	
Output current	Іон	-12	mA	V _{CC} = 2.7 V
		-24 ^{*2}		V _{CC} = 3.0 V to 5.5 V
	IOL	12		V _{CC} = 2.7 V
		24 ^{*2}		V _{CC} = 3.0 V to 5.5 V
Input rise / fall time ^{*1}	t _r , t _f	10	ns/V	

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

2. Duty cycle $\leq 50\%$



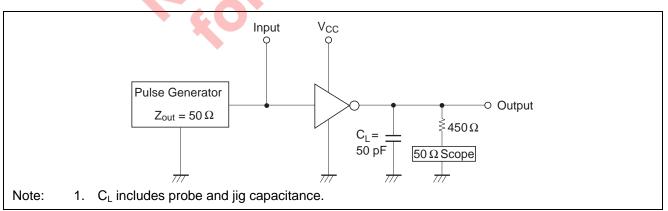
Electrical Characteristics

			Ta = -4	0 to 85°C				
Item	Symbol	V _{cc} (V)	Min	Max	Unit	Test Conditions		
Input voltage	V _{IH}	2.7 to 3.6	2.0		V			
		4.5 to 5.5	V _{CC} ×0.7	—	_			
	V _{IL}	2.7 to 3.6	_	0.8	_			
		4.5 to 5.5	_	V _{CC} ×0.3	_			
Output voltage	V _{OH}	2.7 to 5.5	V _{CC} -0.2		V	I _{OH} = -100 μA		
		2.7	2.2	—	_	$I_{OH} = -12 \text{ mA}$		
		3.0	2.4	—	_	$I_{OH} = -12 \text{ mA}$		
		3.0	2.0		_	$I_{OH} = -24 \text{ mA}$		
		4.5	3.8	_	_	$I_{OH} = -24 \text{ mA}$		
	V _{OL}	2.7 to 5.5	_	0.2	V	I _{OL} = 100 μA		
		2.7	—	0.4	_	I _{OL} = 12 mA		
		3.0	—	0.55	_	l _{oL} = 24 mA		
		4.5	_	0.55	_	$l_{OL} = 24 \text{ mA}$		
Input current	l _{iN}	0 to 5.5	_	±5.0	μA	V _{IN} = 5.5 V or GND		
Quiescent supply current	I _{CC}	5.5	_	20	μA	$V_{IN} = V_{CC}$ or GND		
	ΔI_{CC}	3.0 to 3.6	—	500	μA	V _{IN} = one input at (V _{CC} –0.6)V, other inputs at V _{CC} or GND		

Switching Characteristics

		Ta = -40 to 85°C					From	То
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	(Input)	(Output)
Propagation delay time	t _{PLH}	2.7	G	4.5	7.0	ns	А	Y
	t PHL	3.3±0.3	1.5	3.5	6.0			
		5.0±0.5	—	2.5	5.0			
Input capacitance	CIN	2.7	-02	3.0	_	pF		
Output capacitance	Co	2.7	A	15.0	_	pF		

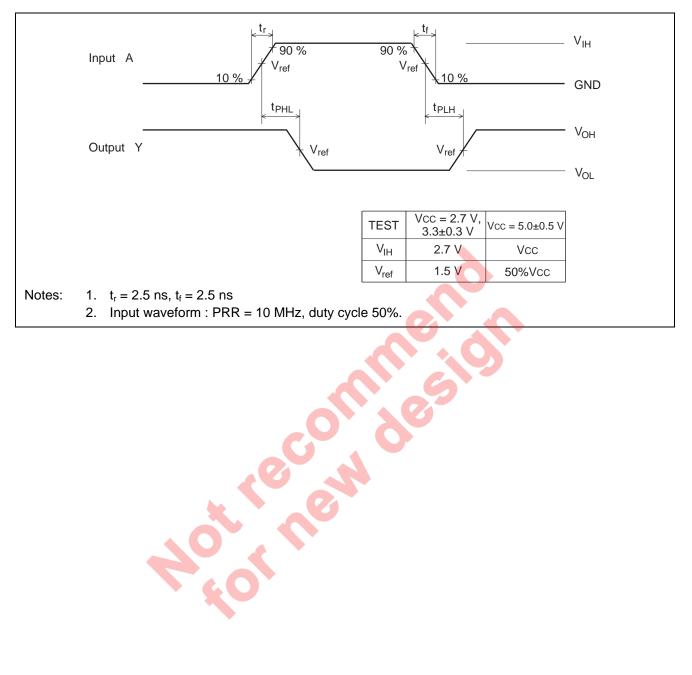
Test Circuit





HD74LVC04

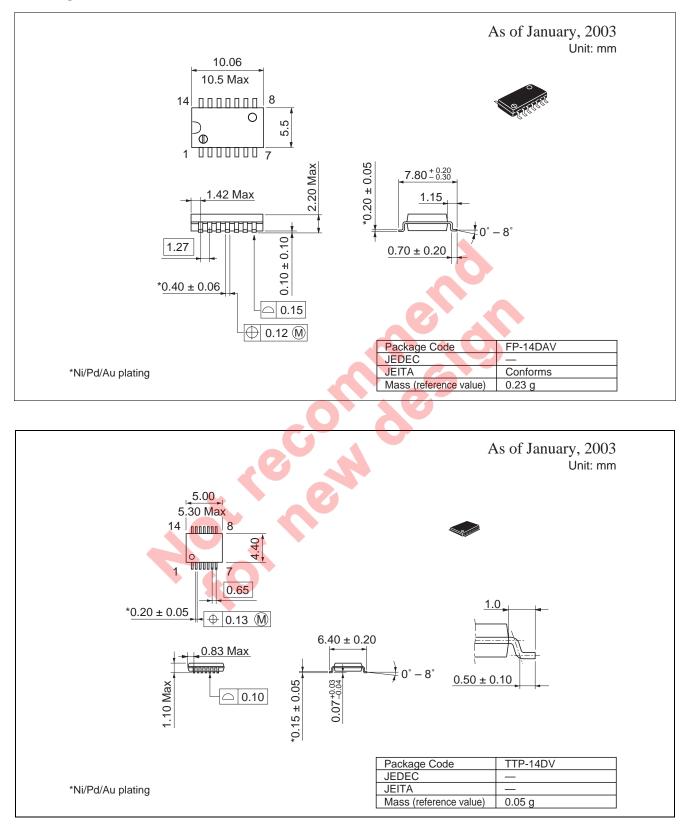
Waveforms



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Package Dimensions



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