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Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# DATA SHEET

# **BIPOLAR ANALOG INTEGRATED CIRCUIT**

 $\mu$ PC1943,1944

## ADJUSTABLE PRECISION SHUNT REGULATORS

### **DESCRIPTION**

The µPC1943, 1944 are adjustable high precision shunt regulators. The output voltage can be set to any value between reference voltage (1.26 V) and 24 V by two external resistors.

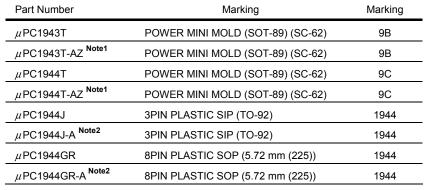
These ICs can apply to error amplifier of switching regulators.

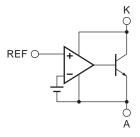
#### **FEATURES**

- Low voltage operation. VREF ≤ VOUT ≤ 24 V
- High accuracy. VREF =  $1.26 \text{ V} \pm 2.4\%$
- Adjustable output voltage by two external resistors.
- Pin compatible with μPC1093. (μPC1944)

#### ORDERING INFORMATION

### **BLOCK DIAGRAM**

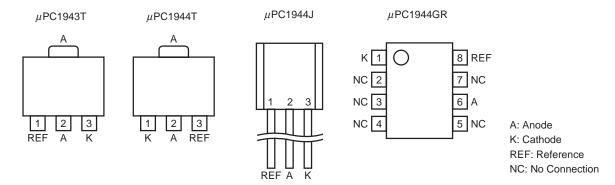




**Notes 1.** Pb-free (This product does not contain Pb in the external electrode.)

2. Pb-free (This product does not contain Pb in the external electrode and other parts.)

#### <R> PIN CONFIGURATIONS (Marking Side)



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The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>' in the PDF file and specifying it in the "Find what:" field.

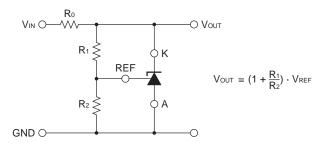
ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified.)

		•		
Parameter		Symbol	Ratings	Unit
Cathode Voltage		VKA	25	V
Cathode Current		lκ	50	mA
Cathode to Anode Reverse	e Current	– Ік	-30	mA
Reference Voltage		VREF	7	V
Reference Input Current		IREF	50	μΑ
Reference to Anode Reverse Current		– Iref	-10	mA
Total Power Dissipation	μPC1943T	Рт	320/1600 Note	mW
	μPC1944T	1	320/1600 Note	mW
	μPC1944J	1 [	560	mW
	μPC1944GR		385	mW
Operating Ambient Temperature		TA	−30 to +85	°C
Operating Junction Temperature		TJ	-30 to +125	°C
Storage Temperature		T <sub>stg</sub>	-65 to +125	°C

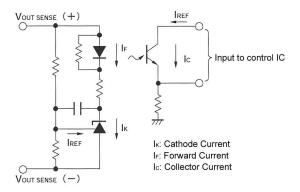
**Note** with 16 cm $^2 \times 0.7$  mm ceramic substrate.

Caution Product quality may suffer if the absolute maximum rating is exceeded even momentarily for any parameter. That is the absolute maximum ratings are rated values at which the product is on the verge of suffering physical damage, and therefore the product must be used under conditions that ensure that the absolute maximum ratings are not exceeded.

# TYPICAL CONNECTION



### <R> APPLICATION CIRCUIT





# RECOMMENDED OPERATING CONDITIONS

Parameter		Symbol	MIN.	TYP.	MAX.	Unit
Cathode Voltage		VKA	VREF		24	V
Cathode Current		lĸ	1	10	30	mA
Total Power Dissipation	μPC1943T	Рт			45/240 Note	mW
	μPC1944T				45/240 Note	mW
	μPC1944J				83	mW
	μPC1944GR				57	mW
Operating Ambient Temperature		TA	-30		+85	°C
Operating Junction Temperature		TJ	-30		+100	°C

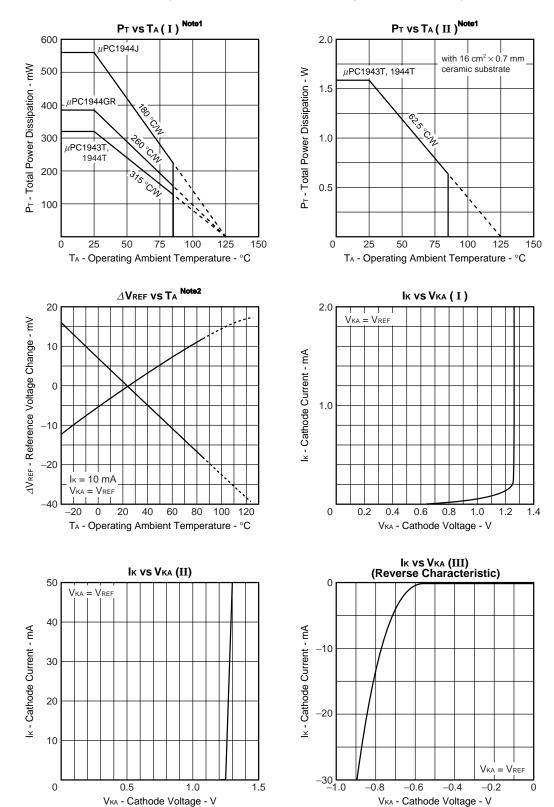
**Note** with  $16 \text{ cm}^2 \times 0.7 \text{ mm}$  ceramic substrate.

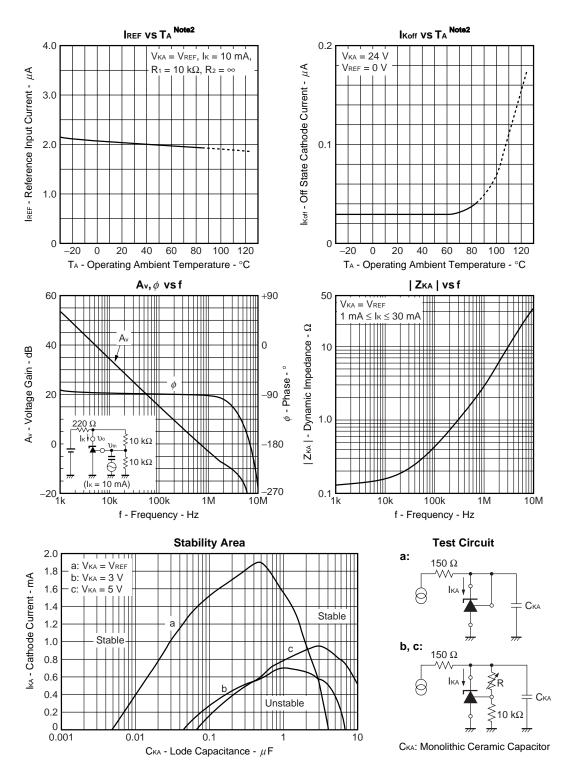
# ELECTRICAL CHARACTERISTICS (IK = 10 mA, TA = 25°C, unless otherwise specified.)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Reference Voltage	V <sub>REF</sub>	V <sub>KA</sub> = V <sub>REF</sub>	1.23	1.26	1.29	V
Reference Voltage Change Over Temperature	△VREF	$V_{KA} = V_{REF}, 0^{\circ}C \le T_{A} \le 70^{\circ}C$		±5	±30	mV
Reference Voltage Change	ΔVREF/ΔVKA	VREF   ≤ VKA ≤ 5 V			2.7	mV/V
Over Cathode Voltage		5 V ≤ VKA ≤ 24 V			2.0	mV/V
Reference Input Current	IREF	$V_{KA} = V_{REF}$ , $R_1 = 10 \text{ k}\Omega$ , $R_2 = \infty$		2.0	4.0	μΑ
Reference Input Current Change Over Temperature	⊿IREF	$V_{KA} = V_{REF}, \ 0^{\circ}C \le T_{A} \le 70^{\circ}C,$ $R_{1} = 10 \ k\Omega, \ R_{2} = \infty$		0.3	1.2	μΑ
Minimum Cathode Current	Kmin	VKA = VREF, △VREF = 2%		0.16	1.0	mA
Off-state Cathode Current	Koff	VKA = 24 V, VREF = 0 V		0.01	1.0	μΑ
Dynamic Impedance	ZKA	$V_{KA} = V_{REF}, f \le 1 \text{ kHz},$ $1 \text{ mA} \le I_K \le 30 \text{ mA}$		0.12	0.5	Ω

# NEC

# TYPICAL CHARACTERISTIC (TA = 25°C, unless otherwise specified. Nominal)





Notes 1. This graph shows the absolute maximum rating, while the other graphs show standard characteristics. Be sure to use the devices within the ranges delimited by the solid lines shown for each device.

2. In this temperature characteristics graph, the ratings for the operating ambient temperatures are indicated by a solid line, and the ratings for the operating junction temperatures are indicated by a dashed line.

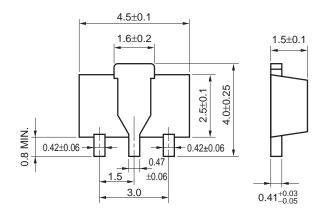
## **Caution of Stability Area**

If the Aluminum electrolytic capacitor is used, it should be kept  $C_{KA} \ge 6.8 \ \mu F$ . Please note Temperature characteristic and Electrical characteristic by capacitor type etc.

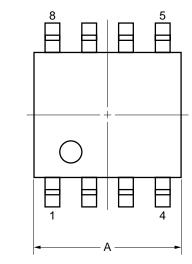


# PACKAGE DRAWINGS (Unit: mm)

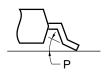
# POWER MINI MOLD (SOT-89) (SC-62)

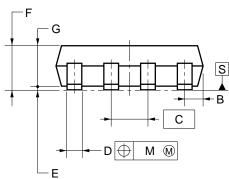


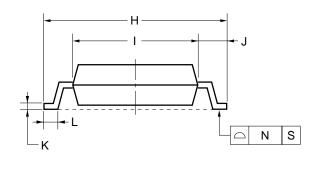
# 8-PIN PLASTIC SOP (5.72 mm (225))



detail of lead end







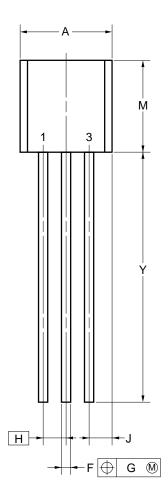
# NOTE

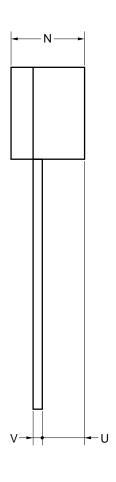
Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
А	$5.2^{+0.17}_{-0.20}$
В	0.78 MAX.
С	1.27 (T.P.)
D	$0.42^{+0.08}_{-0.07}$
E	0.1±0.1
F	1.59±0.21
G	1.49
Н	6.5±0.3
I	4.4±0.15
J	1.1±0.2
K	$0.17^{+0.08}_{-0.07}$
L	0.6±0.2
М	0.12
N	0.10
Р	3°+7°
	COCM ED SSED 6

S8GM-50-225B-6

# 3-PIN PLASTIC SIP (TO-92)





### NOTE

Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
Α	5.0±0.2
F	$0.50^{+0.30}_{-0.10}$
G	0.12
Н	1.27
J	1.33 MAX.
М	5.0±0.5
N	4.0±0.2
U	2.8 MAX.
V	0.50±0.10
Υ	15.0±0.7

P3J-127B-3



### <R> RECOMMENDED SOLDERING CONDITIONS

The  $\mu$ PC1943, 1944 should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, contact an NEC Electronics sales representative.

For technical information, see the following website.

Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

# <R>> Type of Surface Mount Device

# μPC1943T, 1944T : POWER MINI MOLD (SOT-89) (SC-62)

Process	Conditions	Symbol
Infrared ray reflow	Peak temperature: 235°C or below (Package surface temperature), Reflow time: 30 seconds or less (at 210°C or higher), Maximum number of reflow processes: 3 times.	IR35-00-3
VPS	Peak temperature: 215°C or below (Package surface temperature), Reflow time: 40 seconds or less (at 200°C or higher), Maximum number of reflow processes: 2 times.	VP15-00-2
Partial Heating Method	Pin temperature: 350°C or below, Heat time: 3 seconds or less (per each side of the device).	P350

# $\mu$ PC1943T-AZ, 1944T-AZ : POWER MINI MOLD (SOT-89) (SC-62)

Process	Conditions	Symbol
Infrared ray reflow	Peak temperature: 260°C or below (Package surface temperature), Reflow time: 60 seconds or less (at 220°C or higher), Maximum number of reflow processes: 3 times.	IR60-00-3
Partial Heating Method	Pin temperature: 350°C or below, Heat time: 3 seconds or less (per each side of the device).	P350

Caution Apply only one kind of soldering condition to a device, or the device will be damaged by heat stress.



# μPC1944GR : 8PIN PLASTIC SOP (5.72 mm (225))

Process	Conditions	Symbol
Infrared ray reflow	Peak temperature: 235°C or below (Package surface temperature), Reflow time: 30 seconds or less (at 210°C or higher), Maximum number of reflow processes: 3 time.	IR35-00-3
VPS	Peak temperature: 215°C or below (Package surface temperature), Reflow time: 40 seconds or less (at 200°C or higher), Maximum number of reflow processes: 1 time.	VP15-00-1
Wave soldering	Solder temperature: 260°C or below, Flow time: 10 seconds or less, Maximum number of flow processes: 1 time.	WS60-00-1
Partial Heating Method	Pin temperature: 350°C or below, Heat time: 3 seconds or less (per each side of the device).	P350

#### μPC1944GR-A: 8PIN PLASTIC SOP (5.72 mm (225))

Process	Conditions	Symbol
Infrared ray reflow	Peak temperature: 260°C or below (Package surface temperature), Reflow time: 60 seconds or less (at 220°C or higher), Maximum number of reflow processes: 3 time.	IR60-00-3
Wave soldering	Solder temperature: 260°C or below, Flow time: 10 seconds or less, Maximum number of flow processes: 1 time.	WS60-00-1
Partial Heating Method	Pin temperature: 350°C or below, Heat time: 3 seconds or less (per each side of the device).	P350

Caution Apply only one kind of soldering condition to a device, or the device will be damaged by heat stress.

# <R>> Type of Through-hole Device

μPC1944J, 1944J-A: 3PIN PLASTIC SIP (TO-92)

Process	Conditions	Symbol
Wave soldering (only to leads)	Solder temperature: 260°C or below, Flow time: 10 seconds or less.	WS60-00-1
	Maximum number of flow processes: 1 time,	
Partial Heating Method	Pin temperature: 350°C or below,	P350
	Heat time: 3 seconds or less (per each pin).	

Caution For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.

# <R> REFERENCE DOCUMENTS

Review of Quality and Reliability Handbook Information C12769E

Semiconductor Device Mount Manual http://www.necel.com/pkg/en/mount/index.html

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