Unit: mm

② CATHODE

3-2A1S

TOSHIBA Rectifier Silicon Diffused Type

CRG01, CRG02

○ General Power Supply Rectification

• Repetitive peak reverse voltage $: V_{RRM} = 100 \text{ V}, 400 \text{ V}$

• Average forward current $: I_{F(AV)} = 0.7 A$

 Small, thin package suitable for high-density board assembly Toshiba Nickname: "S-FLATTM"

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Repetitive peak reverse voltage	CRG01	\/··	100	$\langle \langle \rangle \rangle$	
	CRG02	V _{RRM}	400	V	
Average forward current		I _{F(AV)}	0,7	A	
Non-repetitive peak forward surge current		I _{FSM}	15 (50 Hz)	A	
			16.5 (60 Hz)		
Junction temperature		Tj (-40 to 150	°C	
Storage temperature		T _{stg}	-40 to 150	//°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.

Weight: 0.013 g (typ.)

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TOSHIBA

operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

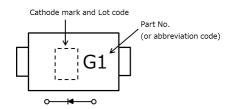
Electrical Characteristics (Ta = 25°C)

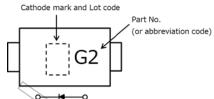
Characteristics	Symbol	Test Condition	Тур.	Max	Unit
	VFM (1)	IFM = 0.1 A (pulse test)	0.88	_	
Peak forward voltage	VFM (2)	I _{FM} = 0.7 A (pulse test)	_	1.1	V
	VFM (3)	I _{FM} = 1.0 A (pulse test)	1.1	_	
Repetitive peak reverse current	IRRM	V _{RRM} = Rated (pulse test)		10	μΑ
Thermal resistance rth (j-a)	\$ FILE (1.1)	Device mounted on a ceramic board board size 50 mm × 50 mm soldering land size 2 mm × 2 mm board thickness 0.64 mm		65	°C/W
	≀th (j-a)	Device mounted on a glass-epoxy board board size 50 mm × 50 mm soldering land size 6 mm × 6 mm board thickness 1.6 mm	_	130	C/VV

Start of commercial production 1998-06

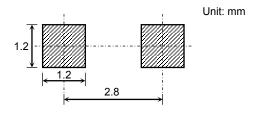
Marking

Abbreviation Code	Part No.		
G1	CRG01		
G2	CRG02		





Land pattern dimensions for reference only



Handling Precaution

1) The absolute maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend when you design a circuit with a device.

V_{RRM}: We recommend that the worst case voltage, including surge voltage, be no greater than 80% of the absolute maximum rating of V_{RRM} for a DC circuit and be no greater than 50% of that of V_{RRM} for an AC circuit. V_{RRM} has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account designing a device at low temperature.

I_{F (AV)}: We recommend that the worst case current be no greater than 80% of the absolute maximum rating of I_{F(AV)}. Carry out adequate heat design. If you can't design a circuit with excellent heat radiation, set the margin by using an allowable Ta max - I_{F(AV)} curve.

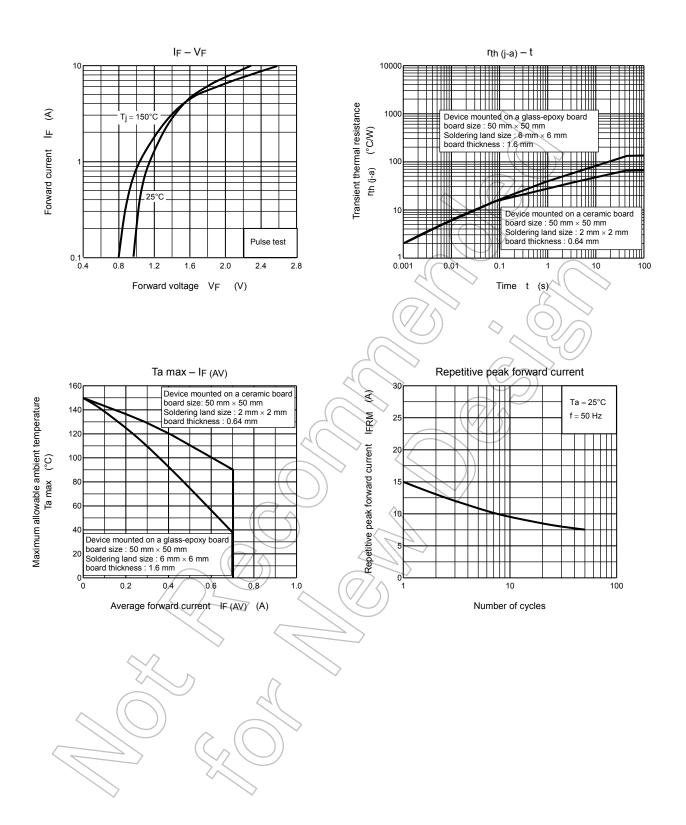
IFSM: This rating specifies the non-repetitive peak current in one cycle of a 50-Hz sine wave, condition angle 180.

Therefore, this is only applied for an abnormal operation, which seldom occurs during the lifespan of the device

Tj: We recommend that a device be used at a Tj of below 120°C under the worst load and heat radiation conditions.

2) Thermal resistance between junction and ambient fluctuates depending on the device's mounting condition. When using a device, design a circuit board and a soldering land size to match the appropriate thermal resistance value.

3) For other design considerations, see the Toshiba website.



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